

INTERFACING ELECTRONIC FOR MEASUREMENT,
SIGNAL PROCESSING AND WIRELESS
COMMUNICATION



Edited by

Sheroz Khan, International Islamic University Malaysia

AHM Zahirul Alam, International Islamic University Malaysia

Anis Nurashikin Nordin, International Islamic University Malaysia



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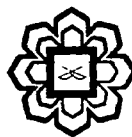
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CONTENTS

Chapter	Title	Page
1	INDUCTIVE SENSOR Atika Arshad, RumanaTasnim, Sheroz Khan, AHM Zahirul Alam	1
2	WIRELESS TRANSFER OF LOW-POWER TO IMPLANTED BIOMEDICAL DEVICES: INTRODUCTION AND 2-D COIL PARAMETERS Imran M. Khan, Sheroz Khan, Othman O. Khalifa	8
3	WIRELESS TRANSFER OF POWER TO LOW-POWER IMPLANTED BIOMEDICAL DEVICES: 3-DIMENSIONAL COIL DESIGN CONSIDERATIONS Imran M. Khan, Sheroz Khan, Othman O. Khalifa	14
4	WIRELESS TRANSFER OF LOW-POWER TO IMPLANTED BIOMEDICAL DEVICES: INDUCTIVE LINK DESIGN Imran M. Khan, Aminullah Khan, Sheroz Khan, Othman O. Khalifa	22
5	WIRELESS TRANSFER OF LOW-POWER TO IMPLANTED BIOMEDICAL DEVICES: RECTIFIER DESIGN Imran M. Khan, Sheroz Khan, Othman O. Khalifa	28
6	DATA CONVERSION BASIC CONCEPTS Ma Li Ya, Sheroz Khan, Anis Nurashikin	36
7	NYQUIST-RATE ANALOG-TO-DIGITAL CONVERTER Ma Li Ya, Sheroz Khan, Anis Nurashikin	41
8	OVERSAMPLING ANALOG-TO-DIGITAL CONVERTER Ma Li Ya, Sheroz Khan, Anis Nurashikin	47
9	SWITCHED-CAPACITOR INTEGRATOR DESIGN Ma Li Ya, Sheroz Khan, Anis Nurashikin	53
10	CMOS OPERATIONAL AMPLIFIER DESIGN Ma Li Ya, Sheroz Khan, Anis Nurashikin	60

11	DIGITAL-TO-ANALOG CONVERTER Ma Li Ya, Sheroz Khan, Anis Nurashikin	68
12	CONVERTERS RESULTS VERIFICATIONS Ma Li Ya, Sheroz Khan, Anis Nurashikin	73
13	DEVELOPMENT OF WEARABLE REFLECTANCE PULSE OXIMETRY FOR TELEHEALTH MONITORING SYSTEM Muhammad Arham, Syed Zulfauzi, Othman O. Khalifa	77
14	DESIGN OF CAPACITIVE MEASURING SYSTEM FOR HIGH FREQUENCY BAND TRANSDUCER Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	83
15	PRINCIPLE OF CAPACITANCE TO VOLTAGE CONVERTER Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	89
16	CMOS OPERATIONAL AMPLIFIER TESTING FOR CAPACITIVE TO VOLTAGE CONVERTER Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	95
17	MATHEMATICAL MODEL FOR CONTACTLESS MEASUREMENT Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	102
18	FREQUENCY RESPONSE OF A CONTACTLESS MEASUREMENT Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	107
19	A MATHEMATICAL STUDY OF A THERMISTOR ASTABLE MULTIVIBRATOR IN A LINEARIZATION TECHNIQUE Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	113

20	A STUDY OF LINEARIZATION TECHNIQUE USING A NONLINEAR THERMISTOR Nurul Arfah binti Che Mustapha, AHM Zahirul Alam, Sheroz Khan	117
21	COGNITIVE RADIO VS INTELLIGENT ANTENNA Siti Rabani Mat Nawi, Nurul Farhah Toha, Khaizuran Abdullah, M. Rafiqul Islam, Sheroz Khan	123
22	UWB PULSE GENERATION AND MODULATION CIRCUITS FOR BIOMEDICAL IMPLANTS Mokhaled M., Mohammed, Sheroz Khan, Jalel Chebil, Khaled A. S. Al-Khateeb, Imran Moez Khan	134
23	UWB COMMUNICATIONS FOR BIOMEDICAL IMPLANTS Mokhaled M. Mohammed, Sheroz Khan, Jalel Chebil, Khalid A. S. Al-Khateeb, Imran Moez Khan	141
24	UWB PULSE GENERATION FOR BIOMEDICAL IMPLANTS Mokhaled M., Mohammed, Sheroz Khan, Jalel Chebil, Khaled A. S. Al-Khateeb, Imran Moez Khan	145
25	ULTRA-WIDE BAND TECHNOLOGY Mokhaled M., Mohammed, Sheroz Khan, Jalel Chebil, Khaled A. S. Al-Khateeb, Imran Moez Khan	149
26	MVL ADC DESIGN AND SIMULATION Soheli Farhana, AHM Zahirul Alam, Sheroz Khan	153
27	MVL DESIGN AND CURRENT MODE CIRCUIT ELEMENTS Soheli Farhana, AHM Zahirul Alam, Sheroz Khan	159
28	NOISE MODULATED CRYPTOGRAPHIC GENERATION FOR USE IN UWB WIRELESS COMMUNICATION Siti HazwaniYaacob, Sigit Puspito Wigati Jarot, Sheroz Khan	164
29	UWB PULSE GENERATION AND SHAPING: ANALYSIS AND SIMULATION RESULTS Zeeshan Shahid, Sheroz Khan, AHM Zahirul Alam	173

30	SIMULATIONS OF RESISTANCE VARIATIONS TO PULSE GENERATOR CIRCUITS	177
	Zeeshan Shahid, Sheroz Khan, AHM Zahirul Alam	
31	PULSE OXIMETRY DESIGN USING ARDUINO BOARD	184
	Muhammad Arham, Syed Zulfauzi and Othman O. Khalifa	

Chapter 8

OVERSAMPLING ANALOG-TO-DIGITAL CONVERTER

MA LI YA, SHEROZ KHAN, ANIS NURASHIKIN

Except Nyquist-rate ADCs, the other kind of analog-to-digital converter (ADC) is oversampling ADCs. With the development of very large scale integration (VLSI), the oversampling ADCs become very popular interface circuits, especially in CMOS technology. In this chapter, an overview of oversampling ADCs is introduced, as well as its most important techniques, that is, oversampling and noise shaping are also discussed.

8.1. SYSTEM ARCHITECTURE

Oversampling conversion is a technique that improves the resolution obtained from straightforward Nyquist-rate conversion (Aziz, 1996). This improvement is achieved through oversampling the input analog signal, which means that the sampling frequency is at a rate significantly faster than the Nyquist rate; normally it is 20 to 500 times faster. Fig. 8.1 is a block diagram for oversampling ADC.

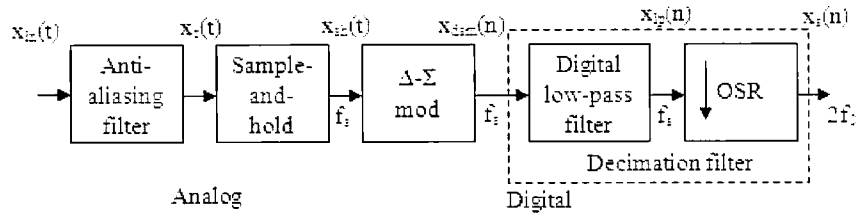


Fig. 8.1: Oversampling conversion system architecture (Aziz, 1996)

This kind of converters modulates the analog signal into a simple code, normally single-bit signal, at a frequency much higher than the Nyquist rate (Norsworthy et al., 1996). In this oversampling conversion system, the most important part (or the key technique) is sigma-delta (Σ - Δ) modulator. The use of high-frequency modulation eliminates the need for abrupt cutoffs in the analog anti-aliasing filter at the input to the ADC. The decimation filter used here is for smoothing the output of the modulator, attenuating noise, interference, and high-frequency components of the signal before they can alias into the signal band when the code is re-sampled at the Nyquist rate (Norsworthy et al., 1996).