ELECTRICAL AUTOMATION SYSTEMS TOWARDS INTELLIGENT AND ENERGY EFFICIENCY APPLICATIONS

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FPGA-BASED HARDWARE MODELING OF LIGHT RAIL TRANSIT FARE CARD CONTROLLER

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21.1 Introduction

The LRT (light rail transit) has become a popular choice for the people to commute throughout the city. However, the current fare system of the LRT is said to be lacking efficiency due to the fact that the current fare system requires the payment to be made in the form of cash and using manual ticketing system which can be a hassle and cause inconvenience for its users (W. Kim 2003 and H. Chang 1999). Therefore, a better system or an improvement is needed to replace the current system.

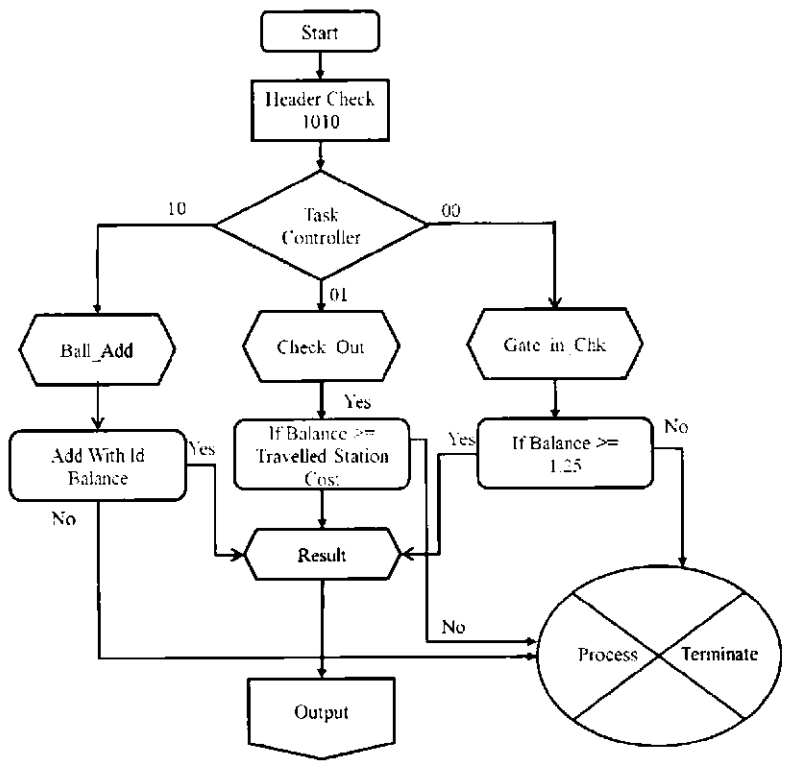

Figure 21.1: Flowchart of complete design