

ANTENNAS AND PROPAGATION

Modeling, Simulation & Measurements

Edited by

MD. RAFIQUUL ISLAM B.Sc., M.Sc., Ph.D., MIEEE
International Islamic University Malaysia

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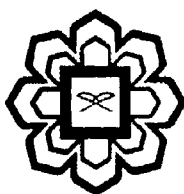
INTERNATIONAL ISLAMIC UNIVERSITY MALAYSIA

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IIUM Press

Published by:
IIUM Press
International Islamic University Malaysia

First Edition, 2011
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Perpustakaan Negara Malaysia

Cataloguing-in-Publication Data

Md. Rafiqul Islam & Jalel chebil: Antennas and Propagation: Modeling, Simulation & Measurements

Bibliography p.
Includes Index
ISBN

ISBN: 978-967-418-138-3

Member of Majlis Penerbitan Ilmiah Malaysia – MAPIM
(Malaysian Scholarly Publishing Council)

Printed By:
IIUM PRINTING SDN.BHD.
NO. 1, JALAN INDUSTRI BATU CAVES 1/3
TAMAN PERINDUSTRIAN BATU CAVES
BATU CAVES CENTRE POINT
68100 BATU CAVES
SELANGOR DARUL EHSAN
TEL: +603-6188 1542 / 44 / 45 FAX: +603-6188 1543
EMAIL: iiumprinting@yahoo.com

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Chapter 4

Design of Symmetrical Fed Patch UWB Antenna Using Partial Ground and Stairs

Md. Rafiqul Islam¹, AHM Zahirul Alam¹,
Muhammad Feroze Akbar J. Khan¹ and Shaker MM. Al-Karaki¹

4.1 Introduction

In this chapter, the calculation of the patch together with the simulation results (by using CST software) and fabricated results has been presented. Based on the literature, there are several techniques that can wider the bandwidth of the rectangular microstrip patch antenna such as [1-4]:

- Partial grounding.
- Addition of stairs on the bottom of the patch antenna.
- Slotted patch antenna.
- Slotted ground.
- Unsymmetrical feed position

As for symmetrical design, an approach to design of ultra wideband antenna using microstrip patch which combines partial ground with stairs on the bottom of the rectangular patch is presented in this chapter. In order to compare with other designs, few parameters such as radiation pattern, antenna gain and bandwidth are considered.

4.2 Design of Symmetrical Microstrip Patch Antenna Using CST Simulation

Before designing an ultra wideband antenna, it is necessary to begin with design of a simple rectangular patch antenna which later we will add stairs and changed the dimension of the ground. Therefore it is very important to design simple patch antenna which can give optimum performance. Following subtopic discussed designing and optimization of simple patch antenna. Transmission line model is used to calculate the dimensions of a patch. There are several main input parameters have to be selected to carry out design of rectangular patch:

- Frequency of operation (f_0): the resonant frequency of the antenna has to be selected. In our project desired frequency of operation is 5.5 GHz.
- Dielectric constant of substrate (ϵ_r): the dielectric material used in this design is metallic with low dielectric constant substrate $\epsilon_r = 5.2$. This is based on availability of Printed Circuit Board (PCB) that we are using in the lab.
- Height of the substrate (h): thickness of substrate is 1.6 mm. This is based on availability of Printed Circuit Board (PCB) that we are using in the lab.

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